

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		09989777
	Filing Date		2001-11-19
	First Named Inventor	Craig Nemecek	
	Art Unit	2123	
	Examiner Name	Mary C. Jacob	
	Attorney Docket Number	CD01208M	

U.S. PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code:	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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	2	6530065	B1	2003-03-04	McDonald et al.	Entire Document
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10	5818444	B1	1998-10-06	Alimpich et al.	Entire Document
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	2	20050143968	A1	2003-08-28	Odom et al.	Entire Document
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7	"VHDL Samples" retrieved on January 29, 2007 from http://www.csee.umbc.edu/help/VHDL/samples/samples.shtml ; 10 pages	<input type="checkbox"/>
8	Anonymous, "Lotus Notes FAQ – How do you generate unique document numbers?" September 19, 1999; retrieved from www.keysolutions.com on July 9, 2008; 1 page	<input type="checkbox"/>
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13	USPTO Non-Final Rejection for Application Number 09/943,062 (CD00228) dated 09/11/2002; 9 pages	<input type="checkbox"/>
14	Snyder et al., "Xilinx's A-to-Z Systems Platform" Cahners Microprocessor, The Insider's Guide to Microprocessor Hardware, February 6, 2001; 6 pages	<input type="checkbox"/>
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18	USPTO Advisory Action for Application Number 09/943,062 (CD00228) dated 09/25/2003; 3 pages	<input type="checkbox"/>
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